

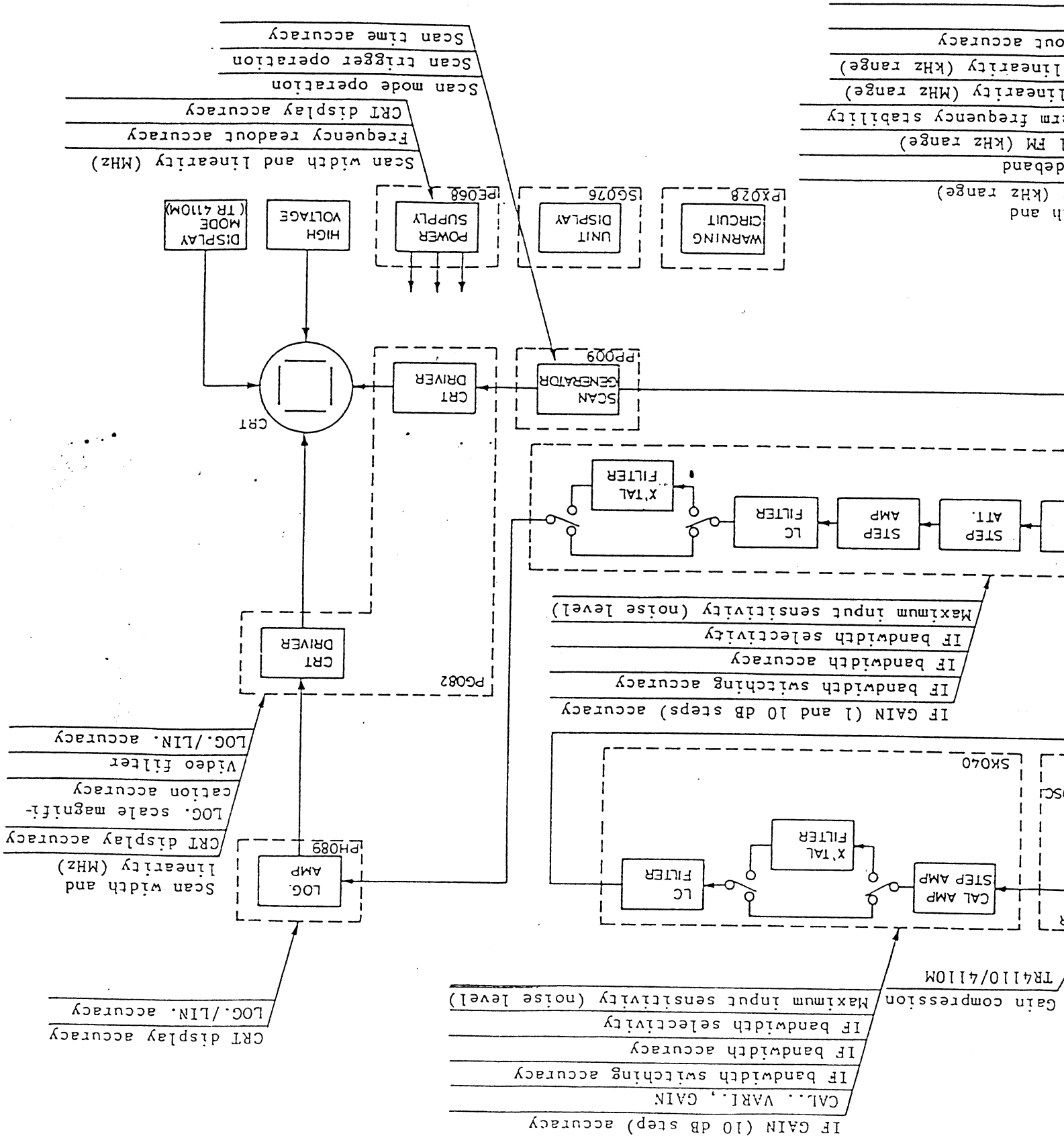


Fig. 1-3 Circuit configuration with

